Notice of References Cited

| Application/Control No. 09/975,864 | Reexamination | Applicant(s)/Patent Under Reexamination HARRISON ET AL. | | |
|------------------------------------|---------------|---------------------------------------------------------|--|--|
| Examiner | Art Unit | | | |
| Mary J. Steelman | 2122 | Page 1 of 1 | | |

U.S. PATENT DOCUMENTS

| * | | Document Number Country Code-Number-Kind Code | Date MM-YYYY | Name | Classification |
|---|---|--------------------------------------------------|-----------------|--------------------|----------------|
| * | А | US-4,667,290 | 05-1987 | Goss et al. | 717/147 |
| * | В | US-5,742,840 | 04-1998 | Hansen et al. | 712/210 |
| * | С | US-6,058,465 | 05-2000 | Nguyen, Le Trong | 712/7 |
| * | D | US-6,113,650 | 09-2000 | Sakai, Junji | 717/160 |
| * | Е | US-2003/0188299 A1 | 10-2003 | Broughton et al. | 717/141 |
| * | F | US-2004/0024536 A1 | 02-2004 | Rognes, Torbjorn | 702/20 |
| * | G | US-2004/0015533 A1 | 01-2004 | Hansen et al. | 708/523 |
| * | Н | US-2004/0073773 A1 | 04-2004 | Demjanenko, Victor | 712/007 |
| | - | US- | | | |
| | J | US- | | | |
| | Κ | US- | | | |
| | ٦ | US- | | | |
| | М | US- | | | |

FOREIGN PATENT DOCUMENTS

| * | | Document Number Country Code-Number-Kind Code | Date MM-YYYY | Country | Name | Classification |
|---|---|--------------------------------------------------|-----------------|---------|------|----------------|
| | N | | | | | |
| | 0 | | | | | |
| | Р | | | | W | |
| | Q | | | | | |
| | R | | | 7 | | |
| | s | | | | | · |
| | Т | | | | | |

NON-PATENT DOCUMENTS

| * | | Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages) |
|---|---|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| | U | Balakrishnan, S.; Nandy, S.K., "Arbitrary precision arithmetic-SIMD style", IEEE 1997, p. 128-132, retrieved from IEEE 8/3/200 |
| | ٧ | Larsen, Samuel; Amarasinghe, Saman, "Exploiting Superword Level Parallelism with Multimedia Instruction Sets", p. 1-11, November 18, 1999, <url: 99="" commit="" papers="" slp-tm.pdf="" www.cag.lcs.mit.edu=""> retrieved 8/3/2004.</url:> |
| | w | Owen, Robert E; Martin, Daniel, "A Uniform Analysis Method for SDP Architectures and Instruction Sets with a Comprehensive Example", p. 528-537, Signal Processing Systems, 1998, retrieved from IEEE 8/3/2004. |
| | х | "MMX technology developers guide: Chapter 4 MMX tm Code Development Strategy", (Intel 1997), retrieved from <url: cps560="" dg_chp4.htm="" hardware="" intel="" mmx="" notes="" www.udayton.edu="" ~cps="">, retrieved 8/3/2004.</url:> |

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).) Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.